



9-1-04

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. .... 10/609,311  
Filing Date ..... June 26, 2003  
Inventor ..... Donald L. Yates  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2811  
Examiner ..... Unknown  
Attorney Docket No. .... MI22-2236  
Customer No. .... 021567  
Title: Semiconductor Processing Patterning Methods and Constructions

**INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. Copies of all references are attached. No admission is made regarding whether all the listed references are prior art.

Citation of the referenced art is respectfully requested.

This Information Disclosure Statement is being filed before the mailing date of a first Office Action. There, no fee is believed to be required. However, in the event that a fee is required for filing this Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated:

8/30/04

By:

Robert C. Hyta  
Reg. No. 46,791



## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	10/609,311
	Filing Date	June 26, 2003
	First Named Inventor	Donald L. Yates
	Art Unit	2811
	Examiner Name	Unknown
Total Number of Pages in This Submission	Attorney Docket Number	MI22-2236

### ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance communication to Technology Center (TC) <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please Identify below): PTO Return Receipt Postcard Form PTO-1449 Copies of all cited references
<b>Remarks</b> Customer No. 021567  NO FEE IS REQUIRED WITH THIS FILING.  However should a fee be determined as owing, please charge such fee to Deposit Account No. 23-0925.		

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Robert C. Hyta/Reg. No. 46,791 Wells St. John, P.S.
Signature	
Date	8/30/04

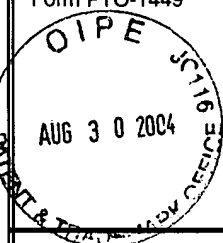
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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2236	SERIAL NO. 10/609,311		
 <p><b>LIST OF ART CITED BY APPLICANT</b> (Use several sheets if necessary)</p>				APPLICANT: Donald L. Yates			
				FILING DATE June 26, 2003	GROUP 2811		
<b>U.S. PATENT DOCUMENTS</b>							
*Examiner's Initials	AA	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	4,086,074	04/78	Minot et al.			
	AB	4,622,735	11/86	Shibata			
	AC	4,683,645	08/87	Naguib et al.			
	AD	4,693,910	09/87	Nakajima et al.			
	AE	4,766,090	08/88	Coquin et al.			
	AF	5,099,304	03/92	Takemura et al.			
	AG	5,236,865	08/93	Sandhu et al.			
	AH	5,444,024	08/95	Anjum et al.			
	AI	5,470,784	11/95	Anjum et al.			
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AJ						
	AK						
	AL						
<b>OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)</b>							
	AM		ABSTRACT: Basceri et al., Atomic Layer Deposition for Nanoscale CU Metalization, 10 pages (pre-April 2004).				
	AN		En et al., Plasma immersion ion implantation reactor design considerations for oxide charging, 85 SURFACE AND COATINGS TECHNOLOGY 64-69 (1966).				
	AO		Ku et al., The Application of Ion Beam Mixing, Doped Silicide, and Rapid Thermal Processing of Self-Aligned Silicide Technology, 137 J. Electrochem. Soc. No. 2, pp. 728-740 (February 1990).				
EXAMINER		DATE CONSIDERED					
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>							

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U.S. PATENT DOCUMENTS							
*Examiner's Initials	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
AA	5,670,298	09/97	Hur				
AB	6,037,239	03/00	Jennings				
AC	6,096,621	08/00	Jennings				
AD	6,130,140	10/00	Gonzalez				
AE	6,133,105	10/00	Chen et al.				
AF	6,133,116	10/00	Kim et al.				
AG	6,156,674	12/00	Li et al.				
AH	6,177,235 B1	01/01	Francou et al.				
AI	6,281,100 B1	08/01	Yin et al.				

FOREIGN PATENT DOCUMENTS							
Document Number	Date	Country	Class	Subclass	Translation		
					Yes	No	
AJ							
AK							
AL							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)		
AM		ABSTRACT: How to Eliminate Voiding in Porous-Low-k Dielectrics and The Mechanism of Void Formation; Lin et al.; 4 pages
AN		COB Stack DRAM Cell Technology beyond 100 nm Technology Node; Yongjik Park & Kinam Kim; pp. 349.1 - 349.3;
AO		Rubin et al., Shallow-Junction Diode Formation by implantation of Arsenic and Boron Through Titanium-Silicide Films and ..., 17 IEEE Transactions on Electron Devices, No. 1, pp. 183-190 (January 1990).
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	AA	6,277,709 B1	08/01	Wang et al.			
	AB	6,277,728 B1	08/01	Ahn et al.			
	AC	6,291,363 B1	09/01	Yin et al.			
	AD	6,380,611 B1	04/02	Yin et al.			
	AE	6,383,723 B1	05/02	Iyer et al.			
	AF	6,440,793 B1	08/02	Divakaruni et al.			
	AG	6,465,325 B2	10/02	Ridley et al.			
	AH	6,720,638 B2	04/04	Tran			
	AI	6,780,728	08/04	Tran			

FOREIGN PATENT DOCUMENTS							
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						Yes	No
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	AL						

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	AA	2001/0006759 A1	07/01	Shipley, Jr. et al.			
	AB	2002/0076879 A1	06/02	Lee et al.			
	AC	2002/0196651 A1	12/02	Weis			
	AD	2003/0013272 A1	01/03	Hong et al.			
	AE						
	AF						
	AG						
	AH						
	AI						

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	AA	10/655,997		Daley			09/03
	AB	10/689,958		Basceri			10/03
	AC	10/690,029		Derderian et al.			10/03
	AD	10/882,118		Sandhu et al.			04/04
	AE	10/879,367		Blalock et al.			06/04
	AF						
	AG						
	AH						
	AI						
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